

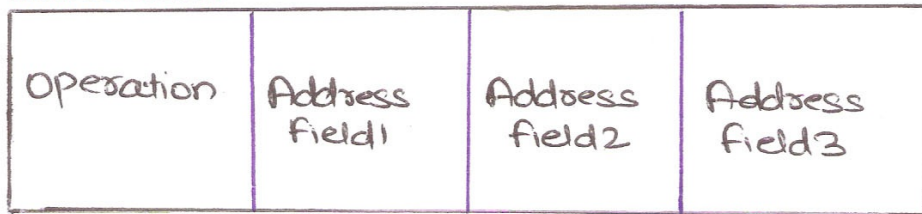
Instruction Set Format (Fixed, Variable, Hybrid)

An Instruction set format is used to define the layout of the bits allocated to these elements of instructions. Or the instruction format explicitly or implicitly indicates the addressing modes used for each operand in that instruction.

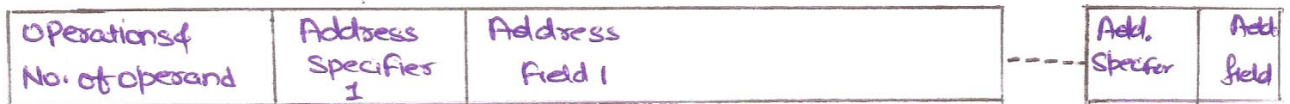
Example of Instruction set Format



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(a) Fixed length (eg ARM, Alpha, MIPS)



(b) Variable length (eg VAX, Intel 80x86)



(c) (Hybrid) IBM 360/70, MIPS 16

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- Each operand requires address specifier then we have to use variable length format
- For simple decoding, predictable operations use fixed length instruction format.



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